DECLARATION FOR PATENT APPLICATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and joint inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled

COLUMN REDUNDANCY FOR DIGITAL MULTILEVEL NONVOLATILE MEMORY

the specification of which (check one) _X_ is attached hereto or was filed on as Application No and was amended on (if applicable).					
I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.					
I acknowledge the duty to disclose all information which is material to patentability as defined in 37 CFR \S 1.56.					
application(s) for pater which designated at lea identified below any fo	t or inventor's certificate ist one country other than	5 U.S.C. § 119(a)-(d) or § 365(b) of any , or § 365(a) of any PCT International at the United States, listed below and hav ent or inventor's certificate having a filin is claimed:	pplication e also		
Prior Foreign Application(s)		<u>Priority Claimed</u> <u>Yes</u> <u>No</u>			
Number	Country	Day/Month/Year Filed	_		
Number	Country	Day/Month/Year Filed			

I hereby claim the benefit un application(s) below.	der 35 U.S.C. § 119	9(e) of any United States provisional
Application Number	Filing Date	_
Application Number	Filing Date	
of any PCT International app the subject matter of each of States application in the mar acknowledge the duty to disc	olication designating the claims of this a mer provided by the close all information e available between	o of any United States application(s), or § 365(c) g the United States, listed below and, insofar as pplication is not disclosed in the prior United of the prior argument of 35 U.S.C. § 112, I which is material to patentability as defined in the filing date of the prior application and the application:
Application Number	Filing Date	Status: Patented, Pending, Abandoned
Application Number	Filing Date	Status: Patented, Pending, Abandoned
statements made on informat statements were made with t punishable by fine or imprise	tion and belief are be he knowledge that vonment or both, und	of my own knowledge are true and that all relieved to be true; and further that these willful false statements and the like so made are let 18 U.S.C. § 1001 and that such willful false plication or any patent issued thereon.
Full name of inventor HIEU	VAN TRAN	
Inventor's signature	Henra	Nam 07/15/03
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Tran, et al. Group Art Unit: Not Yet Assigned

Serial No.: Not Yet Assigned Examiner: Not Yet Assigned

Filed: July 28, 2003

Title: COLUMN REDUNDANCY FOR DIGITAL MULTILEVEL

NONVOLATILE MEMORY

POWER OF ATTORNEY BY ASSIGNEE

Assistant Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313

Sir:

The undersigned, being Assignee of the entire right, title and interest in the aboveidentified application by virtue of an Assignment, a copy thereof is attached, under 37 C.F.R. §3.71, to prosecute the application to the exclusion of the Inventor(s), and grants Power of Attorney and correspondence address as stated herein.

Silicon Storage Technology, Inc. , hereby appoints:

Practitioners at Customer Number: 26379

26379

as attorneys/agents with full power of substitution and revocation to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith.

Assignee: Silicon Storage Technology, Inc.

Date: July 15, 2003

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Title: President and CEO